

R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

SUPPORT FOR CLAIM AMENDMENTS

Support for the amendments to the claims can be found in the drawings as originally filed, for example, in FIGS. 5 and 6, and in the specification as originally filed, for example, on page 3, lines 9-20, on page 4, lines 13-19, on page 6, lines 2-14, on page 10, line 14 through page 11, line 15, and on page 12, lines 6-8. As such, no new matter has been introduced.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1, 3, 7-11 and 17-20 under 35 U.S.C. §102(b) as being anticipated by Haskell et al. (U.S. Patent No. 5,742,343; hereinafter Haskell) has been obviated by amendment and should be withdrawn.

In contrast to Haskell, the presently claimed invention (claim 1) provides a method for decoding a bitstream comprising the steps of (A) receiving a first bitstream, where the first bitstream comprises an intra-only frame picture stream comprising alternating macroblock rows, with each row containing a plurality of vertical lines from a single respective field, (B) generating a first field picture and a second field picture in response to the first

bitstream and (C) generating a second bitstream comprising the first field picture and the second field picture, such that the second bitstream is decodable as interlaced field pictures using an MPEG-2 compliant decoder. Claims 10 and 11 include similar limitations.

Haskell does not appear to disclose or suggest all the steps of (A) receiving a first bitstream, where the first bitstream comprises an intra-only frame picture stream comprising alternating macroblock rows, with each row containing a plurality of vertical lines from a single respective field, (B) generating a first field picture and a second field picture in response to the first bitstream and (C) generating a second bitstream comprising the first field picture and the second field picture, such that the second bitstream is decodable as interlaced field pictures using an MPEG-2 compliant decoder, as presently claimed. Haskell appears silent regarding an intra-only frame picture stream comprising alternating macroblock rows, with each row containing information for a plurality of lines of a single field, as presently claimed. In particular, Applicant has downloaded an electronic version of the Haskell reference and performed a search for the words "alternating", "row", "macroblock" and "macroblock rows", with no such occurrences. Therefore, Haskell does not appear, expressly or inherently, to disclose or suggest each and every element of the presently claimed invention, arranged as in the present claims. As

such, the presently claimed invention is fully patentable over the cited reference and the rejection should be withdrawn.

Claims 2-9 and 12-20 depend, directly or indirectly, from either claim 1 or claim 11 which are believed to be allowable. As such, the presently claimed invention is fully patentable over the cited reference and the rejection should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claim 5 under 35 U.S.C. §103 as being unpatentable over Haskell in view of Ng et al. (U.S. Patent No. 5,185,819; hereinafter Ng) has been obviated by appropriate amendment and should be withdrawn.

The rejection of claims 1-3, 7-14 and 17-20 under 35 U.S.C. §103 as being unpatentable over Takeuchi et al. (U.S. Pub. No. 2002/0028061; hereinafter Takeuchi) in view of Haskell in view of Boyce et al. (U.S. Patent No. 5,592,299; hereinafter Boyce) has been obviated by appropriate amendment and should be withdrawn.

The rejection of claims 4, 6 and 15 under 35 U.S.C. §103 as being unpatentable over Takeuchi in view of Haskell in view of Boyce and Design Choice has been obviated by appropriate amendment and should be withdrawn.

The rejection of claims 5 and 16 under 35 U.S.C. §103 as being unpatentable over Takeuchi in view of Haskell in view of Boyce and Ng has been obviated by appropriate amendment and should

be withdrawn.

In contrast to the cited references, the presently claimed invention (claim 1) provides a method for decoding a bitstream comprising the steps of (A) receiving a first bitstream, where the first bitstream comprises an intra-only frame picture stream comprising alternating macroblock rows, with each row containing a plurality of vertical lines from a single respective field, (B) generating a first field picture and a second field picture in response to the first bitstream and (C) generating a second bitstream comprising the first field picture and the second field picture, such that the second bitstream is decodable as interlaced field pictures using an MPEG-2 compliant decoder. Claims 10 and 11 include similar limitations.

Haskell does not appear to teach or suggest all the steps of (A) receiving a first bitstream, where the first bitstream comprises an intra-only frame picture stream comprising alternating macroblock rows, with each row containing a plurality of vertical lines from a single respective field, (B) generating a first field picture and a second field picture in response to the first bitstream and (C) generating a second bitstream comprising the first field picture and the second field picture, such that the second bitstream is decodable as interlaced field pictures using an MPEG-2 compliant decoder, as presently claimed. Haskell appears silent regarding a bitstream comprising an intra-only frame picture

stream comprising alternating macroblock rows, with each row containing information for a plurality of lines of a single field, as presently claimed. In particular, Applicant has downloaded an electronic version of the Haskell reference and performed a search for the words "alternating", "row", "macroblock" and "macroblock rows", with no such occurrences. Therefore, Haskell does not appear, expressly or inherently, to teach or suggest each and every element of the presently claimed invention. As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

Claims 2-9 and 12-20 depend, directly or indirectly, from either claim 1 or claim 11 which are believed to be allowable. As such, the presently claimed invention is fully patentable over the cited references and the rejection should be withdrawn.

New claims 21 and 22 depend, either directly or indirectly, from claim 1 which is believed to be allowable. As such, the presently claimed invention is fully patentable over the cited references.

Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

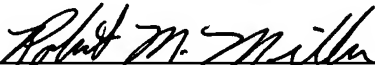
The Examiner is respectfully invited to call the Applicant's representative between the hours of 9 a.m. and 5 p.m. ET at 586-498-0670 should it be deemed beneficial to further

advance prosecution of the application.

If any additional fees are due, please charge Deposit
Account No. 12-2252.

Respectfully submitted,

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c/o
LSI Corporation

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